Analog Optical Receiver Front End

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Abstract - This paper presents an optimized design methodology for an inductor-less 28-Gb/s NRZ optical receiver (ORx) analog front-end (AFE) using the Berkeley Analog Generator (BAG) in 28-nm ...

Abstract—This paper addresses the optimization of the in-terface between the photodetector (PD) and the analog front-end (AFE) in high-speed, high-density optical communication receivers.

In this chapter, we will introduce the basic concept of a high-speed receiver, the integrated circuit (IC) technique of the front-end. Subsequently, passive peaking techniques for a preamplifier are described.

The overall design of a high-speed receiver analog front-end (AFE), optimized for signal processing with a cascaded transimpedance amplifier (TIA) and continuous-time linear equalizer (CTLE), focuses on ...

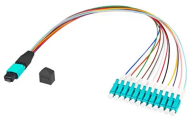
Abstract: In this paper, a 4-channel, 100 Gbps inductorless optical receiver analog front-end fabricated in a 55 nm bulk-CMOS technology is presented. Active feedback technique is widely adopted to ...

This paper presents an analog demultiplexing optical receiver capable of converting a high-baudrate optical input into two half-rate output signals, facilitatin

This paper presents a 10 Gbps optical receiver analog front end and a Mach-Zehnder Modulator (MZM) driver in the 65nm technology. The receiver consists of a Shunt Feedback TIA and ...

In this paper, authors present a front end optical receiver designed with almost GHz bandwidth range and 98 dB transimpedance gain suitable for 10 Gbps optical receiver applications using 180 nm ...

In this paper, a 25 Gbps inductorless optical receiver analog front-end is presented. The inverter-based modified Cherry-Hooper amplifier is proposed and adopted as main stage of the ...



Abstract—We present the design, fabrication, and measurement of a monolithically integrated optical receiver analog front end, where low power operation is a primary consideration ...

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